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distortion compensation and channel leakage

[Search Again](#)**Results:**Journal or Magazine = **JNL** Conference = **CNF** Standard = **STD****1 Optimization analysis of feedforward power amplifier**

Andrenko, A.S.; Horiguchi, K.; Nakayama, M.; Ikeda, Y.; Ishida, O.;
Microwave Conference, 1999 Asia Pacific , Volume: 3 , 30 Nov.-3 Dec. 1999
Page(s): 626 -629 vol.3

[\[Abstract\]](#) [\[PDF Full-Text \(216 KB\)\]](#) **IEEE CNF****2 A novel series diode linearizer for mobile radio power amplifiers**

Yamauchi, K.; Mori, K.; Nakayama, M.; Itoh, Y.; Mitsui, Y.; Ishida, O.;
Microwave Symposium Digest, 1996., IEEE MTT-S International , Volume: 2 , 17
June 1996
Page(s): 831 -834 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(192 KB\)\]](#) **IEEE CNF****3 FIR filters for compensating D/A converter frequency response distort**

Vankka, J.; Lindeberg, J.; Halonen, K.;
Circuits and Systems, 2002. ISCAS 2002. IEEE International Symposium on , Vo
, 26-29 May 2002
Page(s): IV-105 -IV-108 vol.4

[\[Abstract\]](#) [\[PDF Full-Text \(306 KB\)\]](#) **IEEE CNF**

The figure shows a software interface for patent search and management. On the left is a sidebar with a tree view of categories: Drafts, ISNR, BRS, Pending, Active, Active (1), Active (2), Failed, Saved, Favorites, Tagged (0), UDC, Queue, and Trash. The Active (1) node is expanded, showing a single item: 'L1: (1) distortion adj compensation and channel adj leakage'. The main area is a search results table and a detailed patent view.

Search Results Table:

Serial	Document ID	Issue Date	Page	Title	Current IR	Current XRef	Retrieval Classif	Inventor	S	C	P	W	W2
1	US 20030214359 A1	20031120	19	Power amplifying apparatus and radio communications apparatus using same	330/310			Saito, Noboru et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	US 20030118125 A1	20030626	18	Distortion compensation circuit	375/297			Dai, Yoshida	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	US 20030104792 A1	20030605	19	Distortion compensating circuit for compensating distortion occurring in power amplifier	455/115.1			Dai, Yoshida	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	US 20030065761 A1	20030508	24	Distortion reducing circuit	330/149			Okubo, Yoschi et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5	US 20030080814 A1	20030501	33	DISTORTION COMPENSATION APPARATUS AND DISTORTION COMPENSATION METHOD	330/149			Odo, Takayoshi et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
6	US 20030030490 A1	20030212	21	Distortion compensation amplification apparatus of feed forward type and adaptive pre-distortion type	330/149			Nanbu, Yoshinori et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
7	US 20030022639 A1	20030130	21	Peak limiter and multi-carrier amplification apparatus	455/116	455/126; 455/522		Hongo, Naoki et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
8	US 20020171481 A1	20021121	19	High-frequency amplifier circuit and radio communication apparatus using same	330/129			Saito, Noboru et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
9	US 20020167338 A1	20021114	25	Power amplifier and communication device including power amplifier	330/290			Sakuno, Keishi	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
10	US 20020024392 A1	20020729	27	High frequency power amplifier system and wireless communication system	330/310			Maruyama, Masashi et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
11	US 20020021173 A1	20020721	13	Non-linear distortion compensating apparatus	330/149			Yamamoto, Katsuya et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

Detailed Patent View:

Search results: 1 and amplifier

Details for US 20030214359 A1 (20031120, 19 pages):

- Document ID:** US 20030214359 A1
- Issue Date:** 20031120
- Page:** 19
- Title:** Power amplifying apparatus and radio communications apparatus using same
- Current IR:** 330/310
- Current XRef:**
- Retrieval Classif:**
- Inventor:** Saito, Noboru et al.
- Abstract:** A power amplifying apparatus and a radio communications apparatus using the same. The power amplifying apparatus includes a power amplifier, a distortion compensation circuit, and a peak limiter. The distortion compensation circuit compensates for distortion occurring in the power amplifier. The peak limiter limits the peak power of the output signal of the power amplifier. The radio communications apparatus includes a modulator, a power amplifying apparatus, and a demodulator. The power amplifying apparatus is the same as the one described above.
- Claims:** 1-19
- Drawings:** 1-19
- Text:** 1-19
- Tables:** 1-19